

# Interference Search

| Ref # | Hits | Search Query  | DBs      | Default Operator | Plurals | Time Stamp       |
|-------|------|---|----------|------------------|---------|------------------|
| L1    | 0    | (multiplexer and scaler and processor and memory and microcode and (adc or ad or a/d or (analog near1 digital))).clm. | US-PGPUB | OR               | ON      | 2005/08/18 14:37 |
| L2    | 0    | (scaler and processor and memory and microcode and (adc or ad or a/d or (analog near1 digital))).clm.                 | US-PGPUB | OR               | ON      | 2005/08/18 14:37 |
| L3    | 0    | (processor and memory and microcode and (adc or ad or a/d or (analog near1 digital))).clm.                            | US-PGPUB | OR               | ON      | 2005/08/18 14:37 |

# Updated Search

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---|------------------|---------|------------------|
| L4    | 34   | multiplexer and memory and processor and scaler and microcode and (adc or ad or a/d or (analog near1 digital))      | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/08/18 14:42 |
| L5    | 31   | 4 and adjust\$3   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/08/18 14:41 |
| L6    | 31   | 5 and determin\$3   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/08/18 14:41 |
| L7    | 0    | (multiplexer with memory with processor with scaler) and microcode and (adc or ad or a/d or (analog near1 digital)) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/08/18 14:43 |
| L8    | 1    | (multiplexer with memory with processor with scaler) and (adc or ad or a/d or (analog near1 digital))               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/08/18 14:43 |